IOSR Journal of VLSI and Signal Processing (IOSR-JVSP)

Managing Editor Board

- Dr. B. D. Venkatraamana Reddy Dept. of ECE, MITS, A.P, India
- Dr. Mohsin khan Rural Education and Development Foundation, Pakistan
- Dr. S.Sasikumar ANNA university, India
- Dr. P. Karthigaikumar Karunya University, India
- Dr. Rania Ahmed jazan university, Egypt
- Dr. Roukhe Hassane Ismac, Malaysia
- Dr. Aleksandr Cariow West Pomeranian University of Technology, Szczecin, Poland
- Dr. Swapnadip De Jadavpur University, India

Contact Us

Website URL: www.iosrjournals.org Email: Support@iosrmail.org









Qatar Office:

IOSR Journals Salwa Road Near to KFC and Aziz Petrol Station, DOHA, Qatar

India Office:

EHTP, National Highway 8, Block A, Sector 34, Gurugram, Haryana 122001

Australia Office:

43, Ring Road, Richmond Vic 3121 Australia

New York Office:

8th floor, Straight hub, NS Road, New York, NY 10003-9595



IOSR Tournal of VLSI and Signal Processing **IOSR** Journals

International Organization of Scientific Research

p-ISSN: 2319 - 4197 e-ISSN: 2319 - 4200 Volume: 14 Issue: 4

Contents:

Intelligent Threat Detection And Response Systems For Safeguarding Cloud-Hosted Electronic Health Records From Cyber Attacks	01-10
Comparison of different algorithms to improve the quality of Image	11-17
Ultra High multi channel clock frequency(THz, PHz, Ehz, ZHz,YHz,XHz,WHz) baud rate speed PRBS Transceiver HDL RTL ASIC Design for Ultra High long Distance Wireless Communication Engineering Smart Computing Products/Applications	18-26
A Novel Adaptive Random Access Window (RAW) Slot Adjustment Model for IoT Network Using an Integrated Scheme	27-35
Design of a Radiation-Hardened SRAM Cell using 16nm Technology Node	36-43

Peer Reviewed Refereed Journal