

Mitigation of Line Current Harmonics Using Shunt Active Filter With Instantaneous Real and Reactive Power Theory

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Abstract: This paper present the mitigation of line current harmonics using control of shunt active power filter (SAPF). The three phase system is connected to nonlinear load which cause harmonics to affect the power system. The control of shunt active power filter is not only compensating harmonics but also compensate reactive power. Voltage source inverter(VSI) at using SAPF reference current control strategies; instantaneous real and reactive power theory (p-q theory), modified synchronous rotating reference frame theory(Modified d-q theory) with PI control and current control method at using hysteresis current control and SPWM controller. The different control technique of SAPF is based on reference current generation and current control method simulated by using MATLAB Simulink.

Keywords: shunt active power filter, harmonics, three phase system, voltage source converter, p-q theory, d-q theory, PI control, hysteresis current controller, SPWM controller.

I. Introduction

The power system at using time varying load and nonlinear load due to distortion of voltage and current waveforms which leads to failure of equipments. The nonlinear load at using uncontrolled rectifier, adjustable speed drives (ASDs), uninterrupted power supplies (UPS), etc., The traditional passive filter is eliminating harmonics and simplest method but drawbacks of this method are resonant problem, low power factor and large size. So shunt active power filter is most commonly used because of better harmonics compensator and reactive power compensator due to improve the power factor.

The shunt active power filter is based on reference current calculation and current control techniques. The shunt active filter based reference current calculated by using two methods. They are frequency domain method (FDM) and time domain method (TDM). In FDM method reference current calculated based on Fourier Transform (FT), the most widely used FT are: Fast Fourier Transform (FFT) and Wavelet Transform (WT). The drawback of this method leads to a large amount of calculations, making the control method much more complicated and it requires at least one cycle to estimate the reference current, if it is failed to estimate reference current accurately then the current samples are corrupted with noise or transient. Due to this problem in FDM, TDM is used for reference current calculation, so that instantaneous real and reactive power theory (p-q theory), modified synchronous rotating reference frame(SRF) theory (Modified d-q theory) is preferred. This work presents three phase system is connected to nonlinear load cause harmonics compensated based on SAPF. The control of SAPF based on different controller such as reference current calculation methods are p-q theory and modified SRF theory with PI controller and current control methods are hysteresis and SPWM controllers.

II. System Description

In Fig.1 shows the block diagram of three phase shunt active power filter. In three phase system is connected to nonlinear load and SAPF principle is inject current should be same magnitude but opposite phase shift with harmonics current. The harmonics are compensated by voltage source converter, in that three phase shunt active power filter is used.

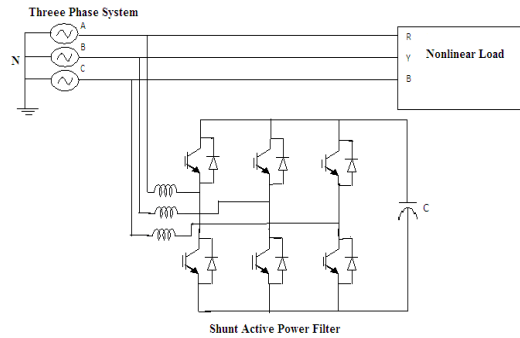


Fig.1 Block diagram of three phase SAPF

III. INSTANTANEOUS REAL & REACTIVE POWER THEORY - (p-q Theory)

The p-q theory is based on set of instantaneous real and reactive power defined in time domain method. It is valid for both steady state and transient state. This theory is efficient and flexible for designing shunt active filter. The three phase voltage and current (abc) are converted into $\alpha\beta 0$ stationary reference frames. The Clarke and inverse Clarke transform of three phase voltage are given by,

$$\begin{bmatrix} V_{00} \\ V_{\alpha} \\ V_{\beta} \end{bmatrix} = \frac{1}{\sqrt{3}} \begin{bmatrix} \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} \\ 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} V_a \\ V_b \\ V_c \end{bmatrix} \quad (1)$$

$$\begin{bmatrix} V_a \\ V_b \\ V_c \end{bmatrix} = \frac{1}{\sqrt{3}} \begin{bmatrix} \frac{1}{\sqrt{2}} & 1 & 0 \\ \frac{1}{\sqrt{2}} & -\frac{1}{2} & \frac{\sqrt{3}}{2} \\ \frac{1}{\sqrt{2}} & -\frac{1}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} V_{\alpha} \\ V_{\beta} \\ V_{00} \end{bmatrix} \quad (2)$$

Similarly three instantaneous line current i_a, i_b, i_c can be converted into $\alpha\beta 0$ coordinate,

$$\begin{bmatrix} i_{00} \\ i_{\alpha} \\ i_{\beta} \end{bmatrix} = \frac{1}{\sqrt{3}} \begin{bmatrix} \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} \\ 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} i_a \\ i_b \\ i_c \end{bmatrix} \quad (3)$$

$$\begin{bmatrix} i_a \\ i_b \\ i_c \end{bmatrix} = \frac{1}{\sqrt{3}} \begin{bmatrix} \frac{1}{\sqrt{2}} & 1 & 0 \\ \frac{1}{\sqrt{2}} & -\frac{1}{2} & \frac{\sqrt{3}}{2} \\ 0 & -\frac{1}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} i_{00} \\ i_{\alpha} \\ i_{\beta} \end{bmatrix} \quad (4)$$

The three phase instantaneous real, reactive and zero sequence power calculated by,

$$\begin{bmatrix} p_{00} \\ p \\ q \end{bmatrix} = \begin{bmatrix} V_{00} & 0 & 0 \\ 0 & V_{\alpha} & V_{\beta} \\ 0 & V_{\beta} & -V_{\alpha} \end{bmatrix} \begin{bmatrix} i_{00} \\ i_{\alpha} \\ i_{\beta} \end{bmatrix} \quad (5)$$

The stationary reference frame at calculating compensated reference current represented by,

$$i_{\alpha} \text{ ref} = \frac{1}{V_{\alpha}^2 + V_{\beta}^2} [V_{\alpha}(P_0 - P) + V_{\beta}q] \quad (6)$$

$$i_{\beta} \text{ ref} = \frac{1}{V_{\alpha}^2 + V_{\beta}^2} [V_{\beta}(P_0 - P) + V_{\alpha}q] \quad (7)$$

The inverse Clarke transform of three phases instantaneous compensated reference current given by,

$$i_{ca\ ref} = \sqrt{\frac{2}{3}} \left[\left(\frac{-i_0}{\sqrt{2}} \right) + i_{ca\ ref} \right] \quad (8)$$

$$i_{cb\ ref} = \sqrt{\frac{2}{3}} \left[\left(\frac{-i_0}{\sqrt{2}} \right) - \frac{1}{2} i_{ca\ ref} + \frac{\sqrt{3}}{2} i_{c\beta\ ref} \right] \quad (9)$$

$$i_{cc\ ref} = \sqrt{\frac{2}{3}} \left[\left(\frac{-i_0}{\sqrt{2}} \right) - \frac{1}{2} i_{ca\ ref} - \frac{\sqrt{3}}{2} i_{c\beta\ ref} \right] \quad (10)$$

The instantaneous real and reactive power theory having some drawbacks, they are non sinusoidal voltage condition at unable to complete compensation of harmonics current.

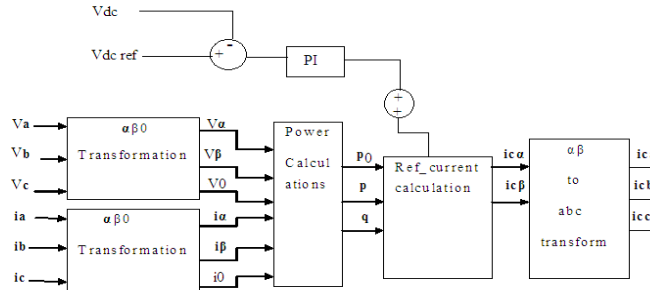


Fig.2 Block diagram of Reference Current Calculation (p-q Theory)

In fig.2 shows the block diagram of reference current calculation, in that Clarke transform to calculate real, reactive and zero sequence power and stationary frame at compensated reference current generate by using PI controller. The SAPF at using DC link capacitor and DC link reference voltage and actual voltage compared to voltage to be regulated. Then inverse Clarke transform to calculate three phase compensated reference current.

IV. Modified Synchronous Rotating Reference Frame Theory -(Modified D-Q Theory)

The Modified d-q theory is based on set of rotating direct and quadrant power defined in time domain method. It is valid for both steady state and transient state. This theory is very efficient and flexible for designing shunt active filter in non sinusoidal voltage condition. The three phase voltage and current (abc) are converted into dq0 synchronous rotating reference frames. The park and inverse park transform of three phase current are given by,

$$\begin{bmatrix} i_{0\alpha} \\ i_{\alpha} \\ i_{\beta} \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} \\ 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} i_a \\ i_b \\ i_c \end{bmatrix} \quad (11)$$

$$\theta = \tan^{-1} \frac{V_{\beta}}{V_{\alpha}} \quad (12)$$

$$\begin{bmatrix} i_d \\ i_q \end{bmatrix} = \begin{bmatrix} \cos(\theta) & \sin(\theta) \\ -\sin(\theta) & \cos(\theta) \end{bmatrix} \begin{bmatrix} i_{\alpha} \\ i_{\beta} \end{bmatrix} \quad (13)$$

The three phase compensated reference current calculated by inverse park transform is given by,

$$i_{ca\ ref} = i_d \sin(\omega t) + i_q \cos(\omega t) \quad (14)$$

$$i_{cb\ ref} = i_d \sin\left(\omega t - \frac{2\pi}{3}\right) + i_q \cos\left(\omega t - \frac{2\pi}{3}\right) \quad (15)$$

$$i_{cc\ ref} = i_d \sin\left(\omega t + \frac{2\pi}{3}\right) + i_q \cos\left(\omega t + \frac{2\pi}{3}\right) \quad (16)$$

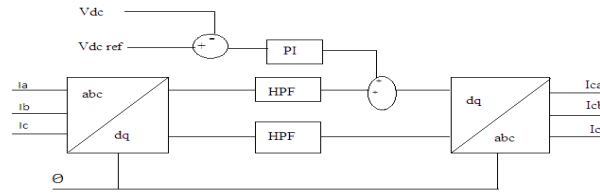


Fig.3 Block diagram of Reference Current Calculation (Modified d-q Theory)

In Fig.3 shows the block diagram of shunt active power filter based Modified d-q theory. The three phases current is converted into rotating reference frame and passed to high pass filter (HPF), it attenuate higher order harmonics. The PI controller is to compare actual and reference DC link voltage to be regulated. The rotating frame coordinates dq converted into three phase compensated reference current Ica, Icb, Icc.

VI. Hysteresis Current Control

The hysteresis current control is used to voltage fed converter to force these converter to behave as controlled ac current source to the power system.

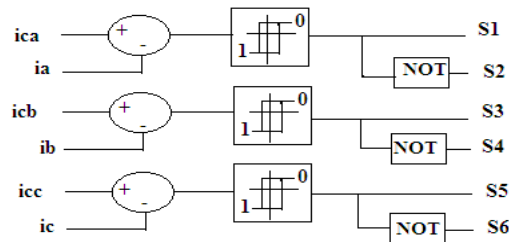


Fig.4 Hysteresis current controller

In Fig.4 shows the hysteresis current controller in that compensated reference current is compared to actual current to triggering of pulses and applied to shunt active filter. In this schemes upper and lower switches on and off condition based on compensated reference current is higher or lower the actual current. Then gate pulse given to shunt active filter of IGBT switches. The drawback of this scheme is increase switching frequency due to increase switching losses and high current ripples.

V. SPWM Controller

In Fig.5 shows the sinusoidal pulse width modulation (SPWM) controller, this method compensating reference current is compared to actual current of each phase and error signal is applied to PI controller. The PI controller output is compared to triangular wave of pulse width modulation techniques to generate gate pulse and applied to shunt active power filter.

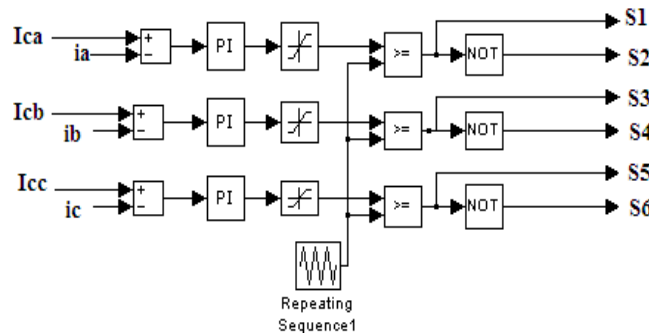


Fig.5 SPWM Controller

VII. Simulation and Result

In Fig.6 shows the simulation diagram of open loop system. The three phase system is connected to nonlinear load in that using three phase diode rectifier with RL load. The source and load side at three phase voltage and current measurement is connected. The source impedance is connected to compensate reactive power in power system.

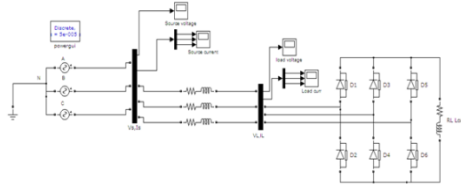


Fig.6 Simulation of open loop system

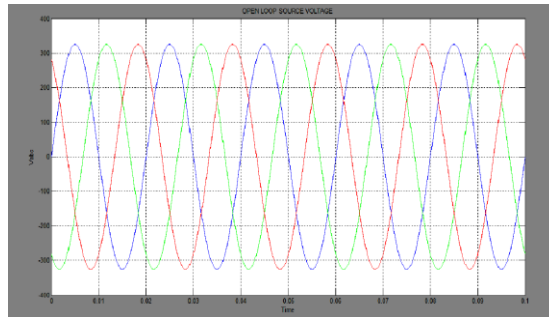


Fig.7 Waveform for open loop system three phase source voltage

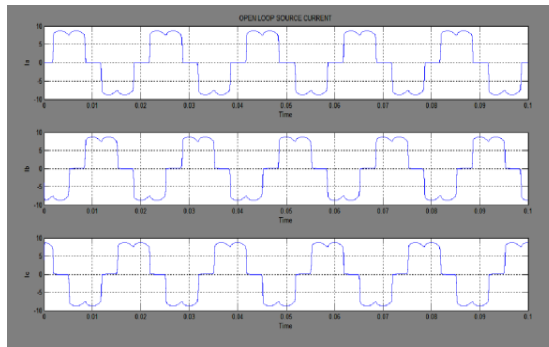


Fig.8 Waveform for open loop system three phase current

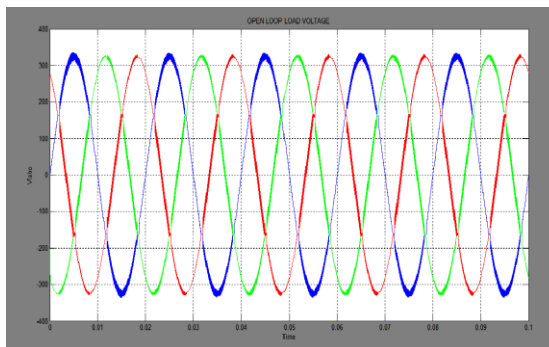


Fig.9 Waveform for open loop system three phase load voltage

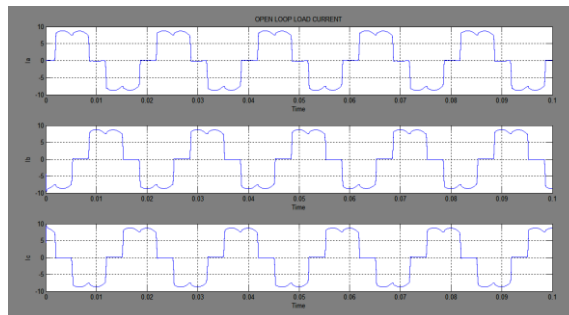


Fig.10 Waveform for open loop system three phase load current

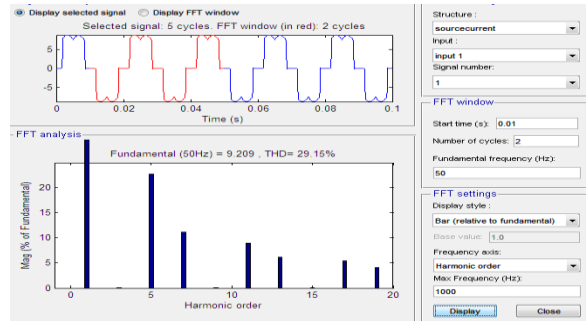


Fig.11 FFT analysis for source current before compensation

In fig.7 and fig.8 shows the output waveform for open loop three phase source voltage V_a, V_b, V_c and current i_a, i_b, i_c . The source current should be distorted due to nonlinear load. In fig.9 and fig.10 shows the output waveform for open loop three phase load voltage V_{La}, V_{Lb}, V_{Lc} and current I_{La}, I_{Lb}, I_{Lc} . The FFT analysis of open loop source current is 29.15%. The source current is sinusoidal by using shunt active power filter.

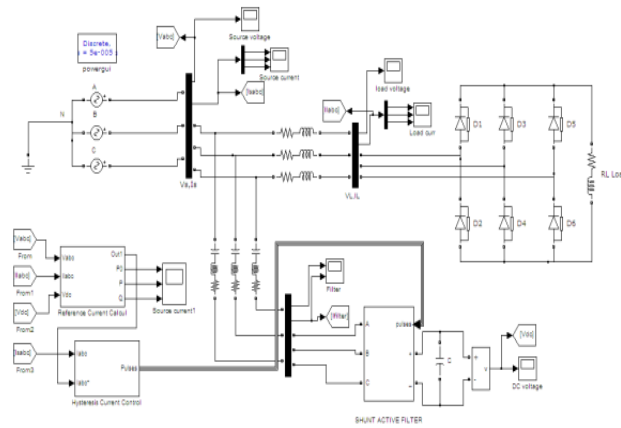


Fig.12 Simulation model of SAPF based p-q theory with hysteresis current control

In fig.12 shows the simulation model of SAPF based p-q theory with hysteresis current control. The point of common coupling at connect shunt active power filter. In fig.13 shows the SAPF based reference current calculation of p-q theory.

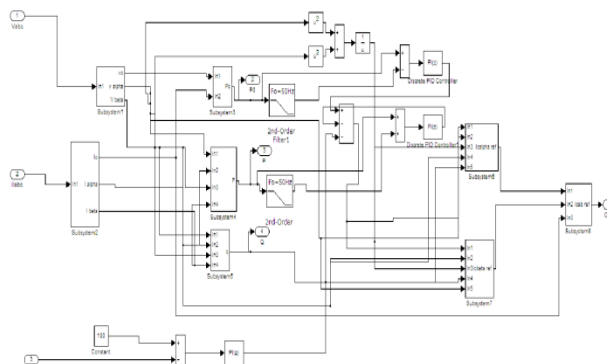


Fig.13 SAPF based p-q theory for reference current calculation

The generated reference current applied to hysteresis current controller and to trigger gate pulses to SAPF. In fig.14 and fig.15 shows the waveform of three phase source voltage and source current in SAPF based p-q theory. The FFT analysis of source current should be 5.30% in SAPF based p-q theory and current controlled by using hysteresis controller.

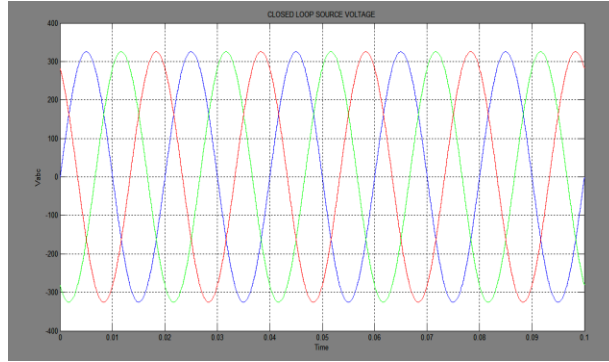


Fig.14 Waveform for three phase voltage SAPF based p-q theory

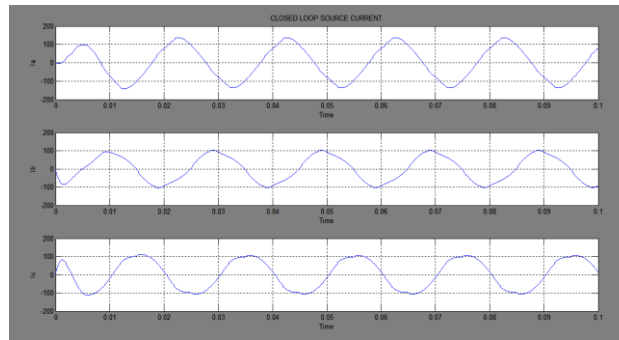


Fig.15 Waveform for three phase current SAPF based p-q theory

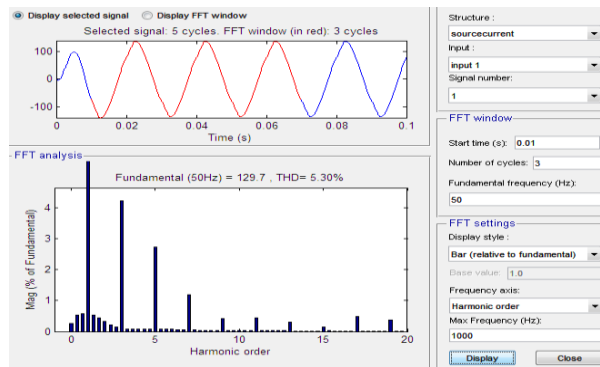


Fig.16 FFT analysis of Source current in SAPF based p-q theory

In fig.17 shows the simulation model of SAPF based modified d-q theory and current controlled by using SPWM controller. In fig.18 shows the reference current calculation based modified d-q theory.

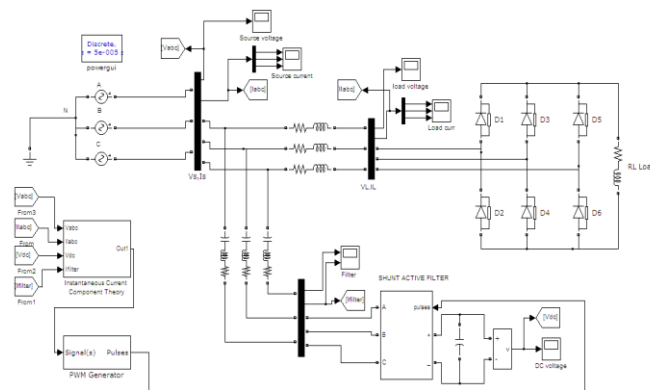


Fig.17 Simulation model of SAPF based modified d-q theory with SPWM control

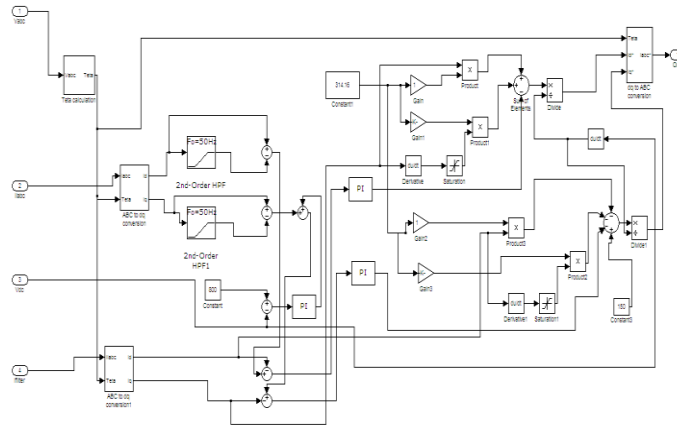


Fig.18 SAPF based modified d-q theory for reference current calculation

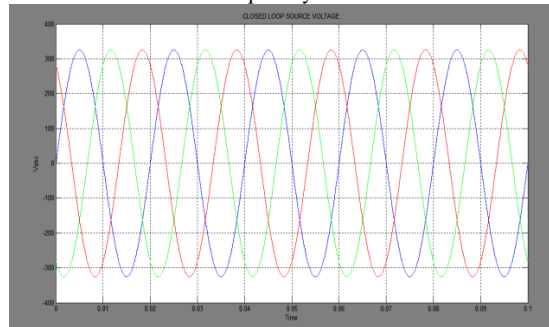


Fig.19 Waveform for three phase voltage SAPF based modified d-q theory

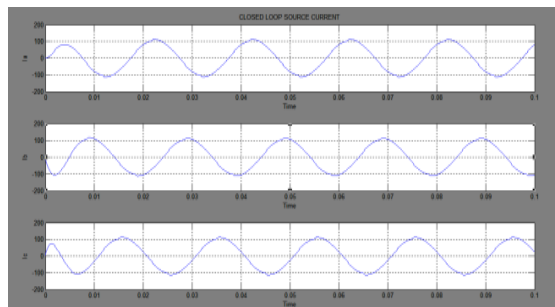


Fig.20 Waveform for three phase current SAPF based modified d-q theory

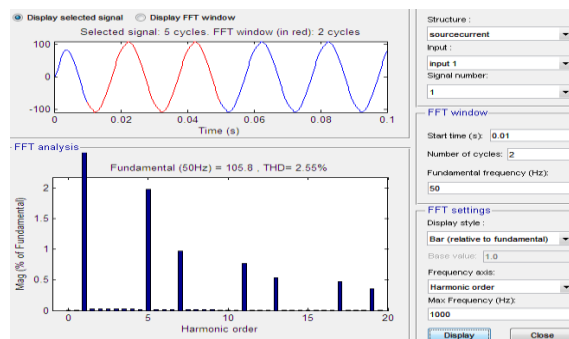


Fig.21 FFT analysis of Source current in SAPF based modified d-q theory

In Fig.19 and Fig.20 shows the three phase source voltage and current. The FFT analysis of source current should be 2.55% in SAPF based modified d-q theory and current controlled by using SPWM controller. Table A.1 gives the system parameters.

Parameter	Value
Supply Voltage	$V_s=230*\sqrt{2}$
Source resistance	$R_s=0.1\Omega$
Source inductance	$L_s=0.5mH$
Filter branch resistance	$R_f=2\Omega$
Filter branch inductance	$L_f=2.5mH$
DC link capacitance	$C_{dc}=3000\mu f$
DC link Voltage	$V_{dc}=800\text{ v}$
PI	$K_p=0.6\ K_i=0.4$
Load resistance	$R_l=15\Omega$
Load inductance	$L_l=60mH$

VIII. Conclusion

In this paper SAPF based two control strategies compared in instantaneous real and reactive power theory and modified d-q theory and current control method at using hysteresis and SPWM controllers has been used for three phase system. In this method in source current harmonics compensated, improved power factor and THD value in IEEE 519 standard which thereby validate the satisfactory system performance.

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