

Simulation analysis of Series Cascade control Structure and anti-reset windup technique for a jacketed CSTR

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Abstract : A method for PID controller tuning based on process models for series cascaded control systems using anti-reset windup technique is proposed in this paper. Closed loop response is obtained using auto tuning of PID controller. This method can be applied to any closed loop stable processes. Closed loop responses of series cascade control loops tuned by the proposed anti-reset windup technique method are compared with the existing cascade control system. Simulation results on nonlinear stable continuous stirred tank reactors is presented to show the efficiency of the proposed controller using anti-reset windup technique.

Keywords: Anti-reset windup technique, CSTR, modelling, Jacket Temperature, PID controller, series cascade structure.

I. Introduction

Cascade control is one of the most successful methods for enhancing single-loop control performance particularly when the disturbances are associated with the manipulated variable or when the final control element exhibits nonlinear behaviour. It is one of the most popular complex control structures that can be found in the process industries implemented in order to improve the disturbance rejection properties of the controlled system [1] [2]. In [3] a relay-feedback based auto tuning method has been used. The procedure still needs of a sequential application of the usual relay based auto tuning approach consuming. Other results provide tuning rules for the primary and secondary controller [4] [5] or suggest alternative control structures based on a modification of the conventional cascade configuration [6]. When the controller is optimally tuned for set-point response, the load-disturbance performance can be very poor [7]. The integral state prediction (ISP) method is used in [8]. The system involves two steps for tuning cascade control systems: first the secondary controller is tuned on the basis of the dynamic model of the inner process. Then, the primary controller is tuned on the basis of the dynamic model of the outer process including the secondary loop. Therefore, if the secondary controller is retuned for some reason, an additional identification step is essential for retuning the primary controller. The phenomenon of reset windup occurs when a controller with integral action operates with the manipulated variable. When the manipulated variable hits the constraint the integral time in a PID controller continues to accumulate the error and overshoot will occur. The contents of the paper are arranged as follows. Section 1 provides the introduction to the temperature control of CSTR using PID. Section 2 deals with the modelling of CSTR and obtaining transfer function for jacket temperature and coolant flow rate. Section 3 deals with analysis of the series cascade structure and auto tuning of controllers. Section 4 explains the anti reset windup protection. Section 5 illustrates the implementation of anti reset windup technique for the controllers using simulink. Section 6 deals with conclusions.

II. Modeling Of Cstr

The examined reactor has real background and graphical diagram of the CSTR reactor as shown in Figure 1. The mathematical model of this reactor comes from balances inside the reactor. Notice that a jacket surrounding the reactor also has feed and exit streams. The jacket is assumed to be perfectly mixed and at lower temperature than the reactor. Energy passes through the reactor walls into jacket removing the heat generated by reaction. The control objective is to keep the temperature of the reacting mixture T , constant at desired value. The only

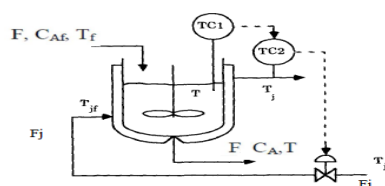


Fig1. Continuous stirred tank reactor with cooling jacket

manipulated variable is the coolant flow rate F_j .

2.1. Steady State Solution

The steady state solution is obtained when, $\frac{dC_A}{dt} = 0$, $\frac{dT}{dt} = 0$, $\frac{dT_j}{dt} = 0$

$$f1(C_A, T, T_j) = \frac{dC_A}{dt} = 0 = F/V(C_{AF} - C_A) - K_0 \exp(-E/RT) C_A \quad (1)$$

$$f2(C_A, T, T_j) = \frac{dT}{dt} = 0 = \frac{F}{V}(T_f - T) + \left(\frac{-\Delta H}{\rho C_p}\right) K_0 \exp(-E/RT) C_A - \frac{UA}{V\rho C_p}(T - T_j) \quad (2)$$

$$f3(C_A, T, T_j) = \frac{dT_j}{dt} = 0 = \frac{F_j}{V_j}(T_{JF} - T_j) + UA \frac{UA(T - T_j)}{V_j \rho_j C_{pj}} \quad (3)$$

Where $-\Delta H$ is the heat of reaction, U is the heat transfer coefficient, A is the heat transfer area, T is the reactor temperature, and T_j is the coolant temperature in the jacket. C_{AF} is the concentration of A in feed. V_j is the volume of the jacket.

Table 1: Reactor Parameter's value

Reactor parameters	Values
F/V,hr-1	1
K ₀ ,hr-1	16.96*10 ¹²
(-ΔH),kcal/kmol	5960
E,kcal/kmol	11843
ΔC _p ,kcal/m ³ °C	500
T _{jf} ,°C	25
UA/V,kcal/m ³ °C.hr	150
T _j ,°C	25

2.2 LINEARIZATION

The goal of the linearization procedure is to find a model with the form

$$\dot{X} = Ax + Bu \quad (4)$$

$$y = Cx + Du \quad (5)$$

Where the states, inputs and output are in deviation variable. Using the parameters given in above table we can find the state space model and transfer function for PID controller design. Hence the transfer function for PID control is given by equation 6.

$$G_{P1} = \frac{1.458S + 11.65}{S^2 + 3.434S + 3.557} \quad (6)$$

Using all reactor parameter's value state space model for cascade system is determined.

$$A = \begin{bmatrix} -7.999 & -0.013674 & 0 \\ 2922.9 & 4.5564 & 1.4582 \\ 0 & 4.7482 & 5.8977 \end{bmatrix}$$

$$B = \begin{bmatrix} 0 \\ 0 \\ -3.2558 \end{bmatrix}$$

$$C = \begin{bmatrix} 0 & 0 & 1 \\ 0 & 1 & 0 \end{bmatrix}$$

$$D = \begin{bmatrix} 0 \\ 0 \end{bmatrix}$$

G_{P1} is the transfer function between (T/T_j)

$$G_{P1} = \frac{4.7476S + 37.9377}{3.2558S^2 + 11.1820S + 11.260} \quad (7)$$

G_{P2} is the transfer function between (T_j/F_j)

$$G_{P2} = \frac{-3.2558s^2 - 11.182s - 11.2606}{s^3 + 9.3322s^2 + 16.7904s - 34.9297} \quad (8)$$

The feed stream concentration is 0.132 lbmol / ft and an 50% conversion of propylene oxide has been determined to be reasonable. Since 50% of propylene oxide is converted to propylene glycol, the propylene glycol concentration is 0.066 lb mol /ft³. In this process, it is seen that the process has inverse response with overshoot. To overcome this problem and to obtain the desired response, we are using series cascade control. The desired parameters for the PID controller are the proportional gain (K_p) integral gain (K_i) and the differential gain (K_D) can be calculated by the automatic PID tuning method in Matlab software.

III. Cascade Structures And Controller Designs

Cascade control has two objectives. The first is to suppress the effect of disturbances on the primary process output via the action of a secondary or inner control loop around a secondary process measurement. The second is to reduce the sensitivity of the primary process variable to gain variations of the part of the process in the inner control loop. Figure 2 shows the conventional cascade control system. Consider a process consisting of two parts process 1 and process 2. Process 1 output has to be controlled. For the cstr shown in figure 1, process I is the reaction in the tank and the controller output is the temperature T. Process II is the jacket and its output is T_j which affects the process I and consequently T

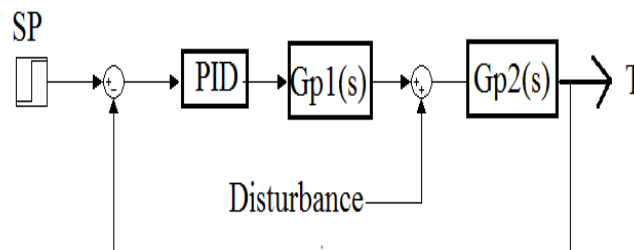


Fig 2: Block diagram representation of conventional feedback control system

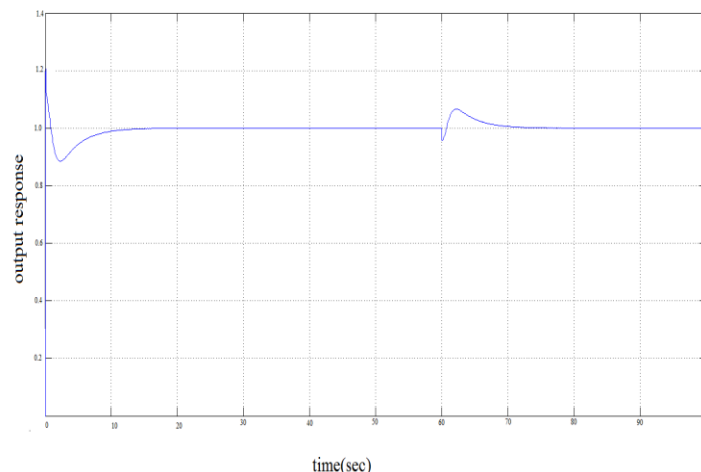


Fig 3: Step Output response of single loop conventional control system

Fig 4 shows the series cascade control system and its step output response is shown in figure 5. There are two control loops using two different measurements T and T_j but sharing a common manipulated variable F_j . In this strategy the reactor temperature controller is the primary controller while the jacket temperature controller is the secondary controller. This is effective because the jacket temperature dynamics are normally significantly faster than the reactor temperature dynamics. An inner loop disturbance such as jacket feed temperature will be felt by the jacket temperature before it has a significant effect on the reactor temperature. The inner loop controller then adjusts the manipulated variable. The jacket temperature controller manipulates the jacket flow rate.

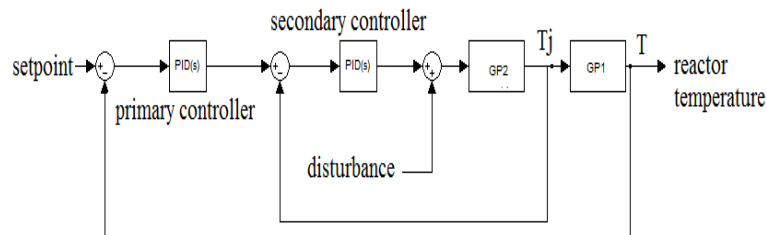


Fig:4 Block diagram representation of series cascade control system

Primary and secondary controllers are tuned for the processes I and II. Table 2 shows the tuned parameter values of primary and secondary processes.

Table 2: Tuned parameters for process I and process II

Parameter/type	Rise time(sec)	Settling time(sec)	% overshoot	Peak time(sec)
Process I	0.695	2.08	9.75	1.1
Process II	0.0813	1.69	10.4	1.1

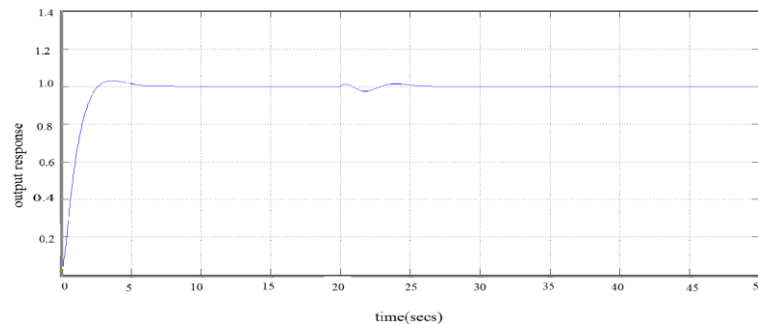


Fig 5: Step input representation of series cascade control system

IV. Anti Reset Windup Protection

Saturation problems can arise when both primary and secondary controllers have automatic reset. The need for anti-reset windup is much greater for cascade designs since once the secondary loop is saturated, the primary controller will also saturate. When putting a cascade system into automatic operation the secondary controller must be first transferred to automatic. Primary and secondary controllers are tuned for the processes I and II.

V. Illustration Of Anti-Reset Windup Compensation

Reset windup is problematic in cascade control systems, particularly when batch processes are involved. The outer loop of a cascade strategy is normally tuned for much slower dynamics than is the inner loop. If the inner loop performs more slowly than expected due to constraints on the manipulated input, then the performance of the outer loop may suffer, so external reset windup protection is provided. Simulation is performed with anti windup on the secondary controller. There is still some windup involved causing overshoot of the primary process output shown in figure 6.

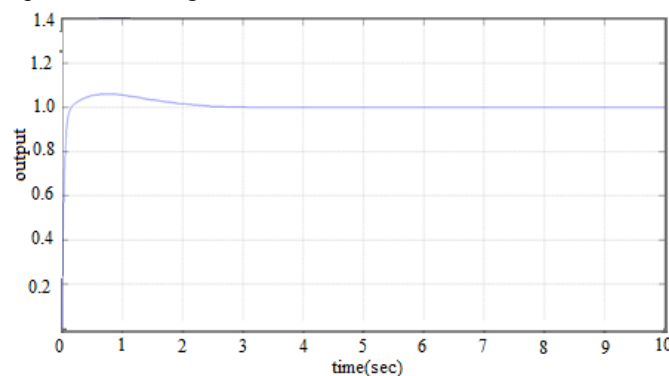


Fig 6: Step Output response of secondary controller

This is because the output of primary controller is the set point for the secondary controller. The secondary controller does not track its set point because of the saturation of the manipulated input. Improved performance can be obtained using an external input to the primary controller. Here the jacket temperature is compared with the jacket temperature set point. If the jacket temperature is not equal to its set point, some of the integral action will be removed by corrective feedback action. The improved cascade control with anti windup on the secondary loop shown in figure 7 and external resetting anti wind up on the primary loop shown in figure 8 has improved performance. The step output responses for the primary and secondary processes are shown in figures 10&11 respectively. Simulink block diagram is shown in figure 9.

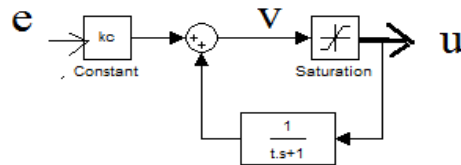


Fig 7: Anti-wind up strategy using first order feedback for the secondary loop

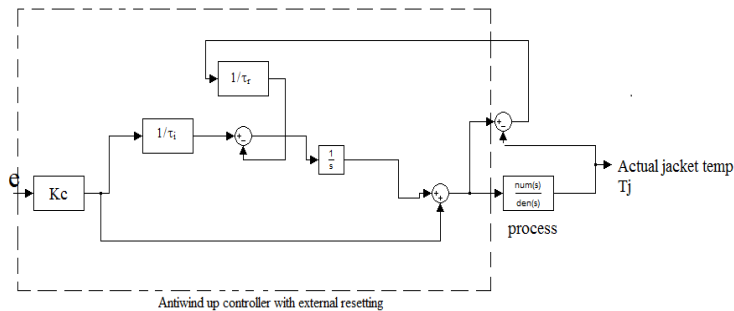


Fig 8 :Reactor temperature controller with external variable reset

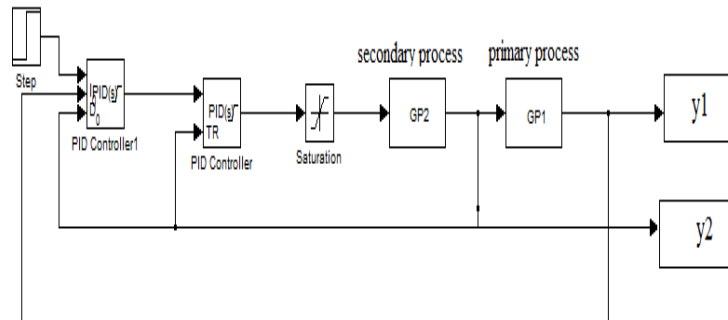


Fig 9: Cascade control simulation using anti reset windup technique on both the controllers

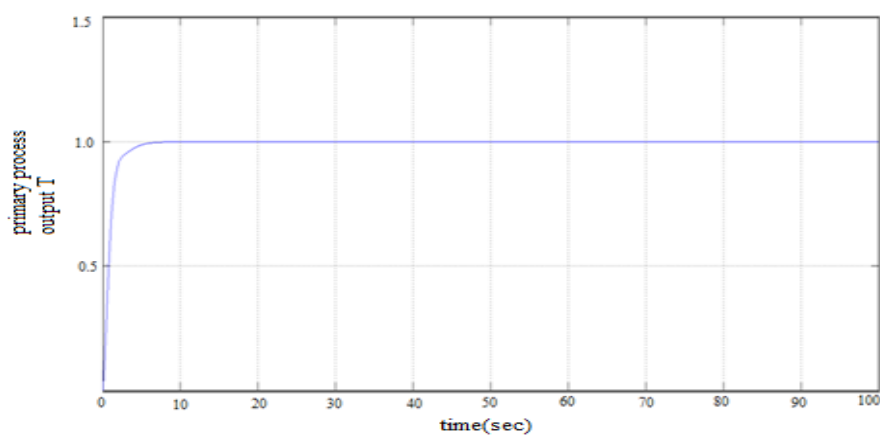


Fig 10: Step Output response of primary process output

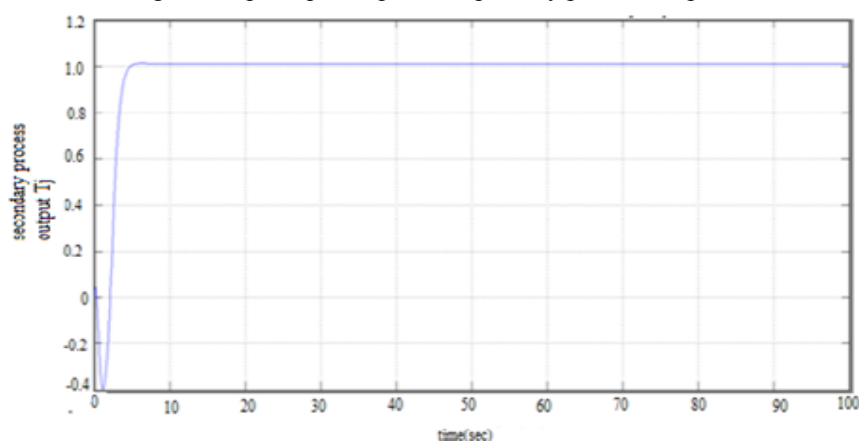


Fig 11: Step Output response of secondary process output T_2

II. Conclusion

Anti-reset windup for the outer loop is implemented by stopping integration when the control effort saturates in order to use the calculated integral time constant. If the anti-reset windup for the outer loop is implemented via a lag around the inner loop, then the lag time constant is not necessarily the same as the computed integral time constant. Outer loop set point tracking and disturbance rejection is generally better than that achievable with a single-loop control system because the inner loop serves to reduce the apparent gain uncertainty of the inner loop process. Simulation results show that the responses with anti-windup has no overshoot and less settling time.

Nomenclature

Notation	Description
F_j	Jacket Feed flow rate
V	Reactor volume
K_0	Reaction rate coefficient
E_a	Activation energy
R	Ideal gas constant
ΔH	Heat of reaction
C_{Af}	Concentration of A in feed
T	Reactor temperature
ρ	Density
C_p	Heat capacity of solution

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